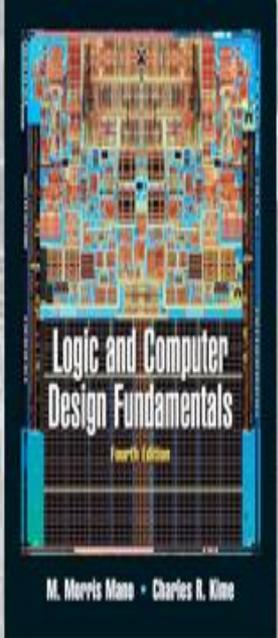


**Universidade Federal de Campina Grande**  
**Unidade Acadêmica de Sistemas e Computação**  
**Curso de Bacharelado em Ciência da Computação**

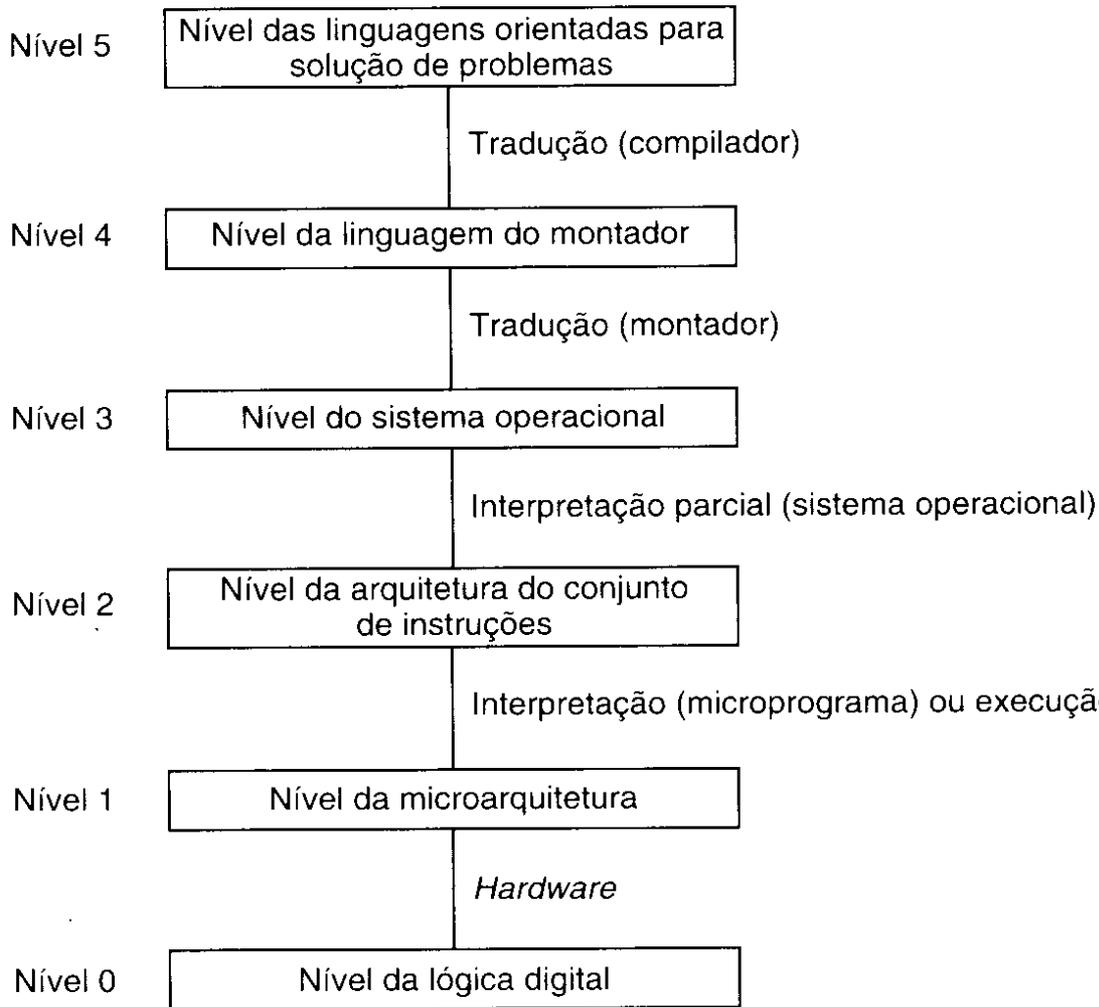
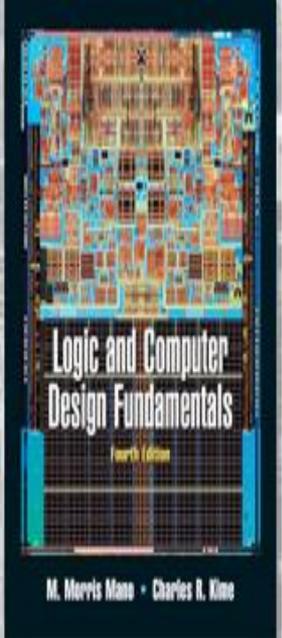
# **Organização e Arquitetura de Computadores I**



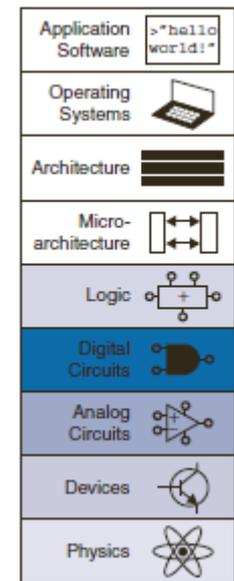
## **Considerações Finais**

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Carga Horária: 60 horas



Application Software	programs
Operating Systems	device drivers
Architecture	instructions registers
Micro-architecture	datapaths controllers
Logic	adders memories
Digital Circuits	AND gates NOT gates
Analog Circuits	amplifiers filters
Devices	transistors diodes
Physics	electrons



## Máquinas Multiníveis Modernas

